TOSHIBA Transistor Silicon P Channel MOS Type

SSM6J07FU

Power Management Switch High Speed Switching Applications

- Small package
- Low on resistance
 - : $R_{on} = 450 \text{ m}\Omega \text{ (max) (V}_{GS} = -10 \text{ V)}$
 - : $R_{on} = 800 \text{ m}\Omega \text{ (max) (V}_{GS} = -4 \text{ V)}$

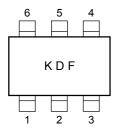
Maximum Ratings (Ta = 25°C)

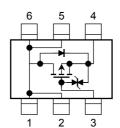
| Characteristics | | Symbol | Rating | Unit | |
|---------------------------|-------|------------------------|---------|------|--|
| Drain-source voltage | | V_{DS} | -30 | V | |
| Gate-source voltage | | V_{GSS} | ±20 | V | |
| Drain current | DC | I _D | -0.8 | А | |
| | Pulse | I _{DP} | -1.6 | | |
| Drain power dissipation | | P _D (Note1) | 300 | mW | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature range | | T _{stg} | -55~150 | °C | |

Note 1: Mounted on FR4 board (25.4 mm \times 25.4 mm \times 1.6 t, Cu Pad: 0.32 mm $^2 \times$ 6)

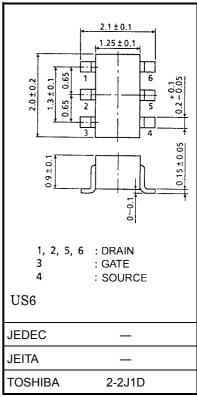
Marking

Equivalent Circuit Figure 1: (top view)



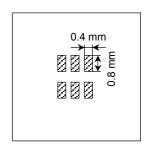


Unit: mm



Weight: 6.8 mg (typ.)

25.4 mm \times 25.4 mm \times 1.6 t, Cu Pad: 0.32 mm² \times 6



Handling Precaution

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

Electrical Characteristics (Ta = 25°C)

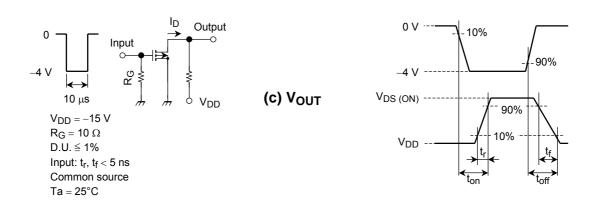
| Charac | eteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|------------------------------|---------------|----------------------|---|------|------|------|------|
| Gate leakage curre | ent | I _{GSS} | $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$ | _ | _ | ±1 | μΑ |
| Drain-source breal | kdown voltage | V (BR) DSS | $I_D = -1 \text{ mA}, V_{GS} = 0$ | -30 | _ | _ | V |
| Drain cut-off curre | nt | I _{DSS} | $V_{DS} = -30 \text{ V}, V_{GS} = 0$ | _ | _ | -1 | μА |
| Gate threshold vol | tage | V_{th} | $V_{DS} = -5 \text{ V}, I_D = -0.1 \text{ mA}$ | -1.1 | _ | -1.8 | V |
| Forward transfer a | dmittance | Y _{fs} | $V_{DS} = -5 \text{ V}, I_D = -0.4 \text{ A}$ (Note2) | 0.7 | _ | _ | S |
| Drain-source ON resistance | | R _{DS} (ON) | $I_D = -0.4 \text{ A}, V_{GS} = -10 \text{ V}$ (Note2) | _ | 350 | 450 | mΩ |
| | | | $I_D = -0.4 \text{ A}, V_{GS} = -4 \text{ V}$ (Note2) | _ | 570 | 800 | |
| | | | $I_D = -0.4 \text{ A}, V_{GS} = -3.3 \text{ V}$ (Note2) | _ | 0.7 | 1.6 | Ω |
| Input capacitance | | C _{iss} | $V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$ | | 130 | _ | pF |
| Reverse transfer capacitance | | C _{rss} | $V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$ | | 16 | _ | pF |
| Output capacitance | | Coss | $V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$ | _ | 52 | _ | pF |
| Switching time | Turn-on time | t _{on} | $V_{DD} = -15 \text{ V}, I_D = -0.4 \text{ A},$ | _ | 28 | _ | ns |
| | Turn-off time | t _{off} | $V_{GS} = 0 \sim -4 \text{ V}, R_G = 10 \Omega$ | _ | 38 | _ | ns |

Note 2: Pulse test

Switching Time Test Circuit

(a) Test circuit

(b) V_{IN}



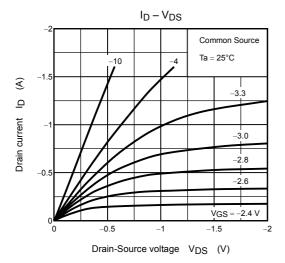
Precaution

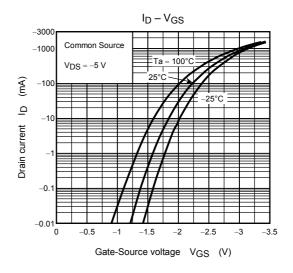
 V_{th} can be expressed as voltage between gate and source when low operating current value is I_D = $-100~\mu A$ for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

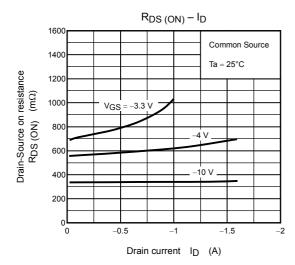
(relationship can be established as follows: $VGS (off) < V_{th} < V_{GS} (on)$)

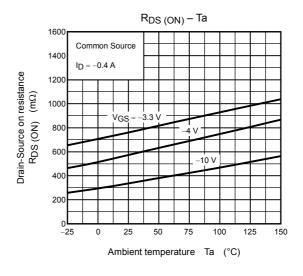
Please take this into consideration for using the device.

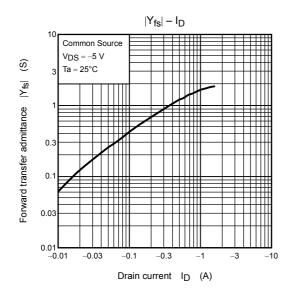
 $V_{\rm GS}$ recommended voltage of $-4.0~{
m V}$ or higher to turn on this product.

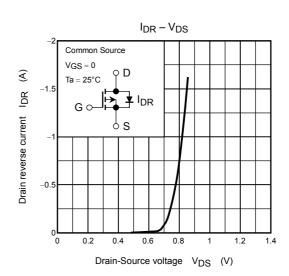




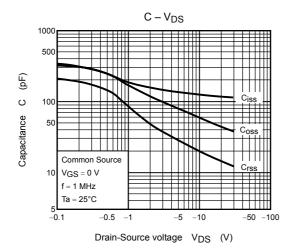


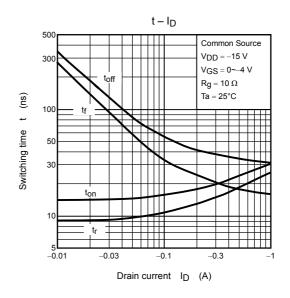


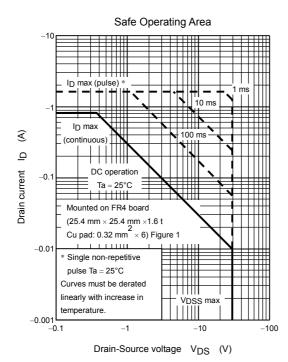


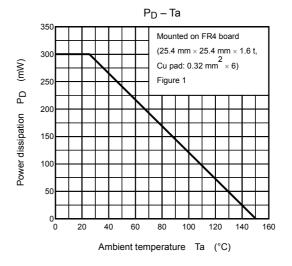


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